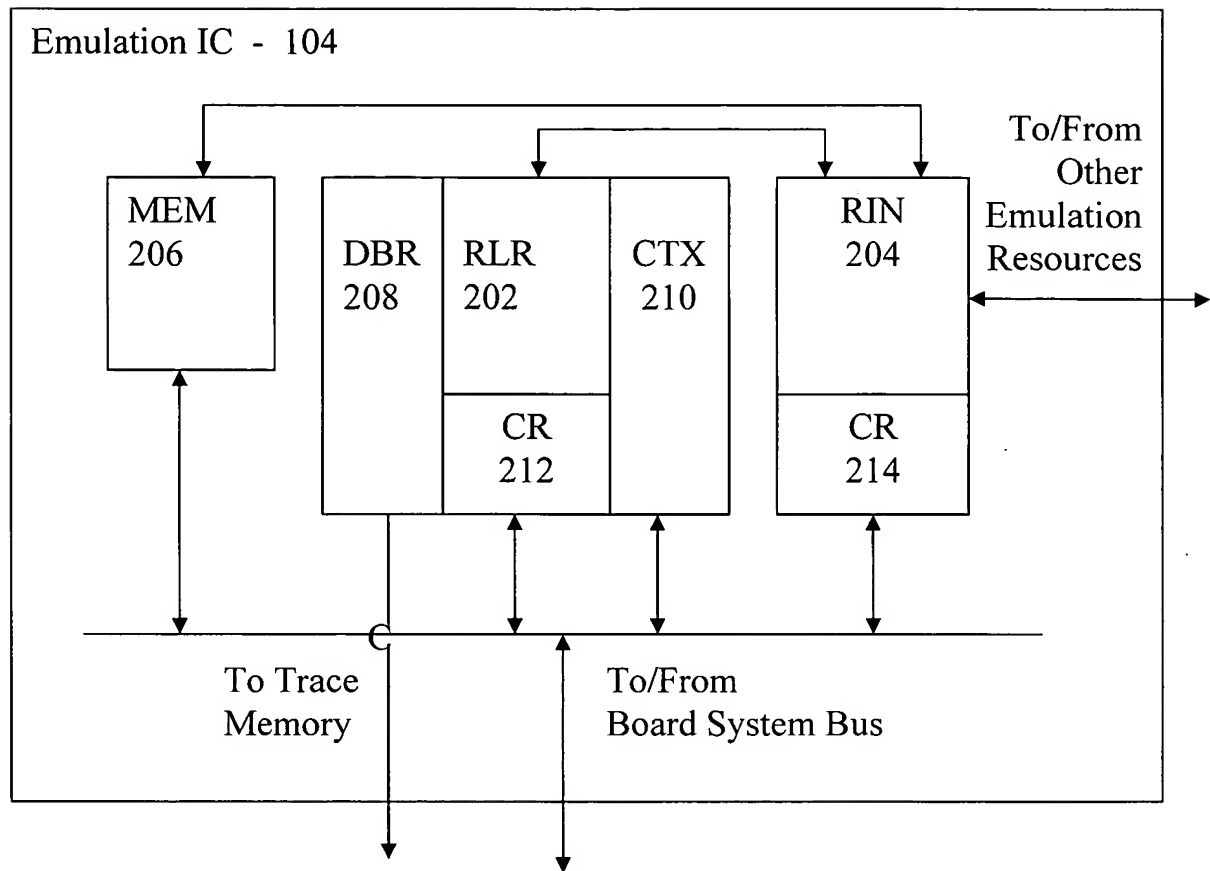


Figure 1



RLR = Reconfigurable Logic Resources
 RIN = Reconfigurable Interconnect Network
 MEM = Memory
 DBR = Debugging Resources
 CTX = Context (State) Elements
 CR = Configuration Registers.

Figure 2a

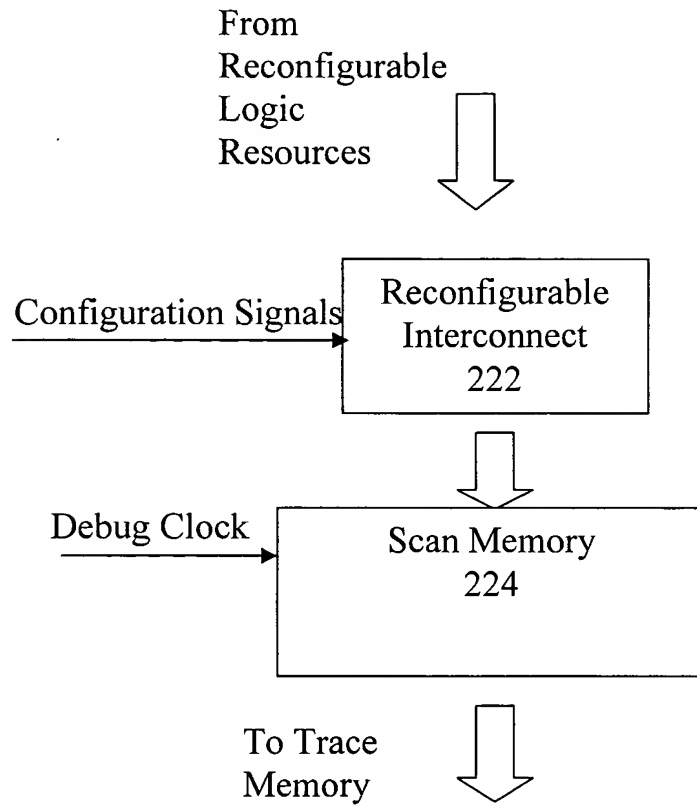


Figure 2b

On-Board Data Processing Resources - 102

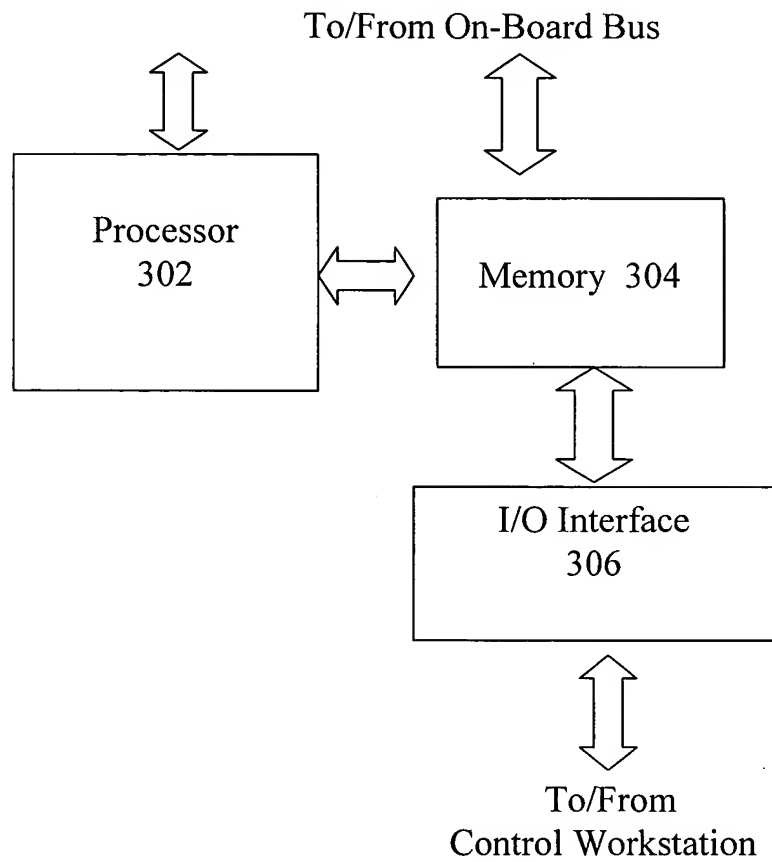


Figure 3

Software - 400

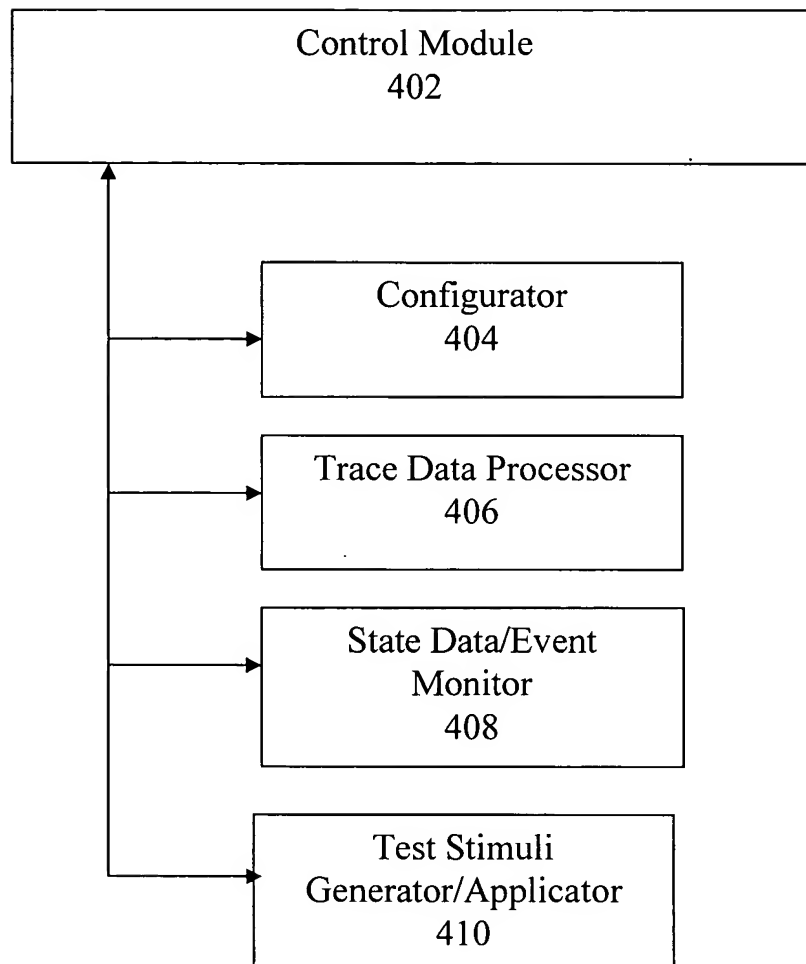


Figure 4

500

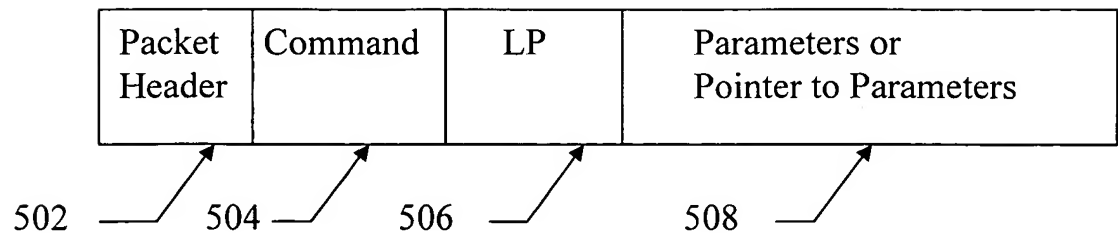


Figure 5

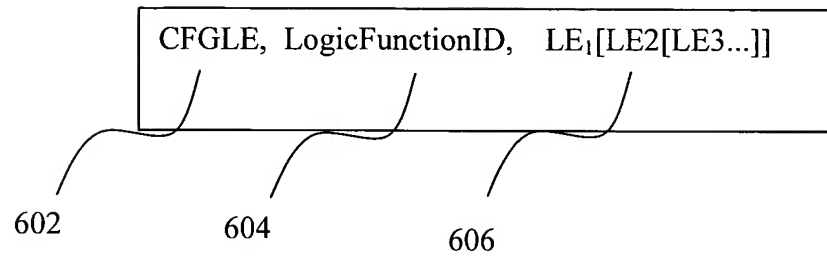


Figure 6a

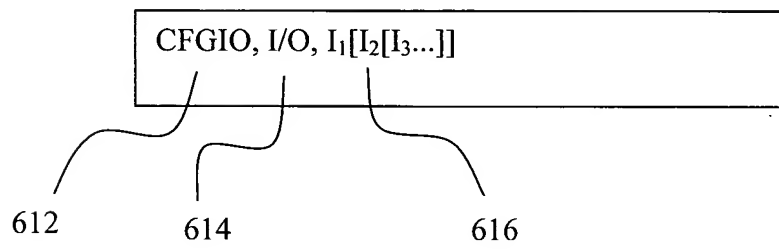


Figure 6b

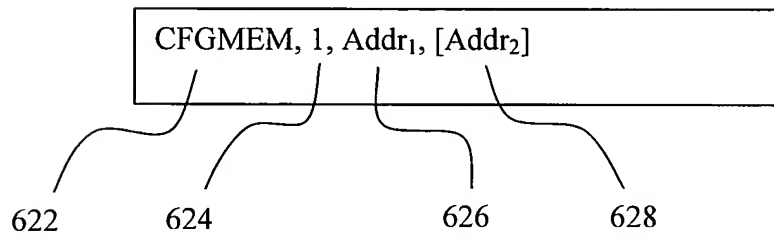


Figure 6c

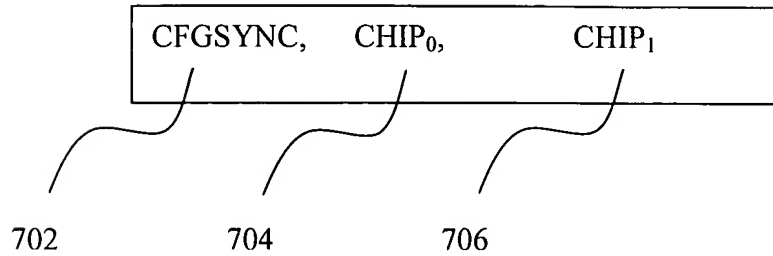


Figure 7a

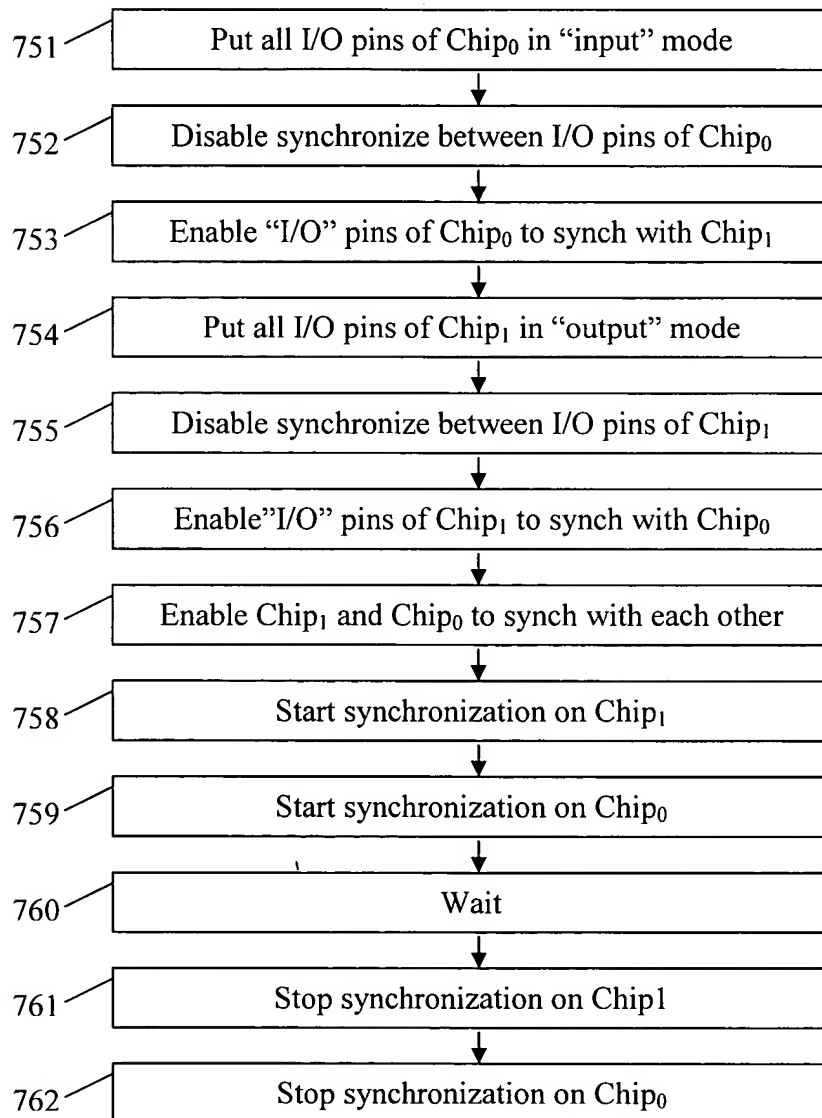


Figure 7b

Emulator System - 800

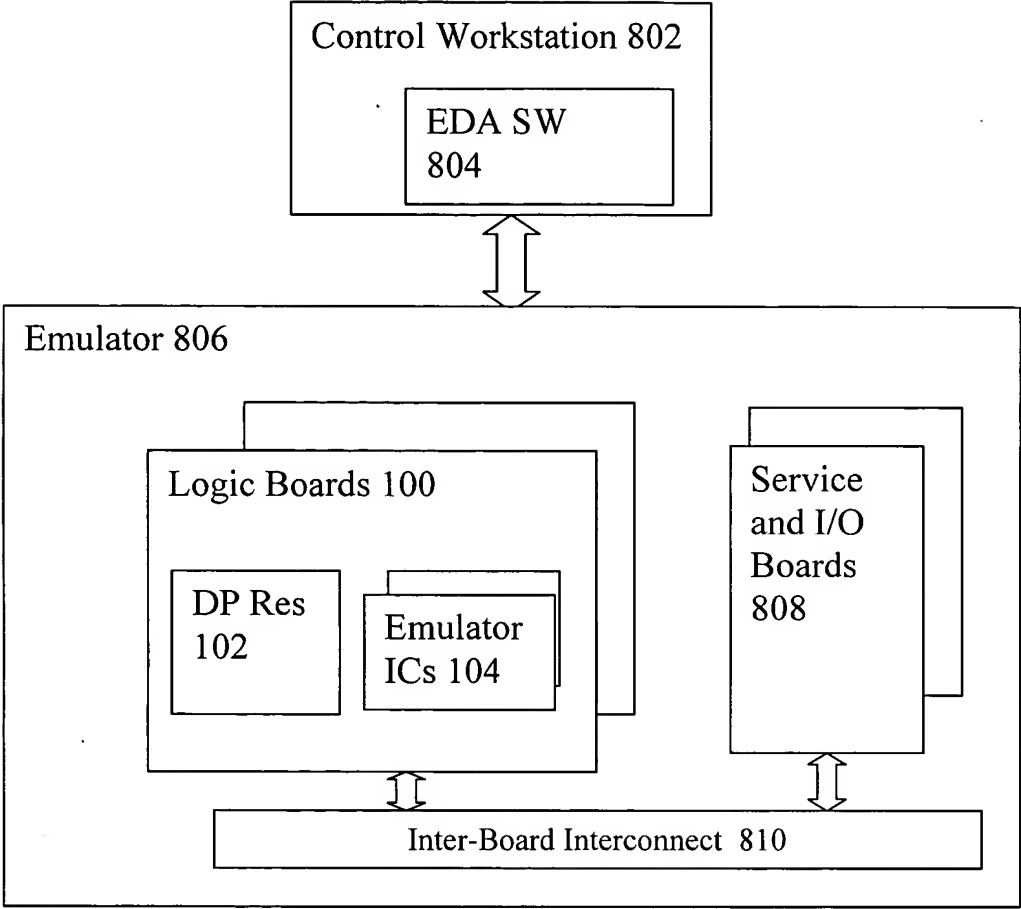


Figure 8

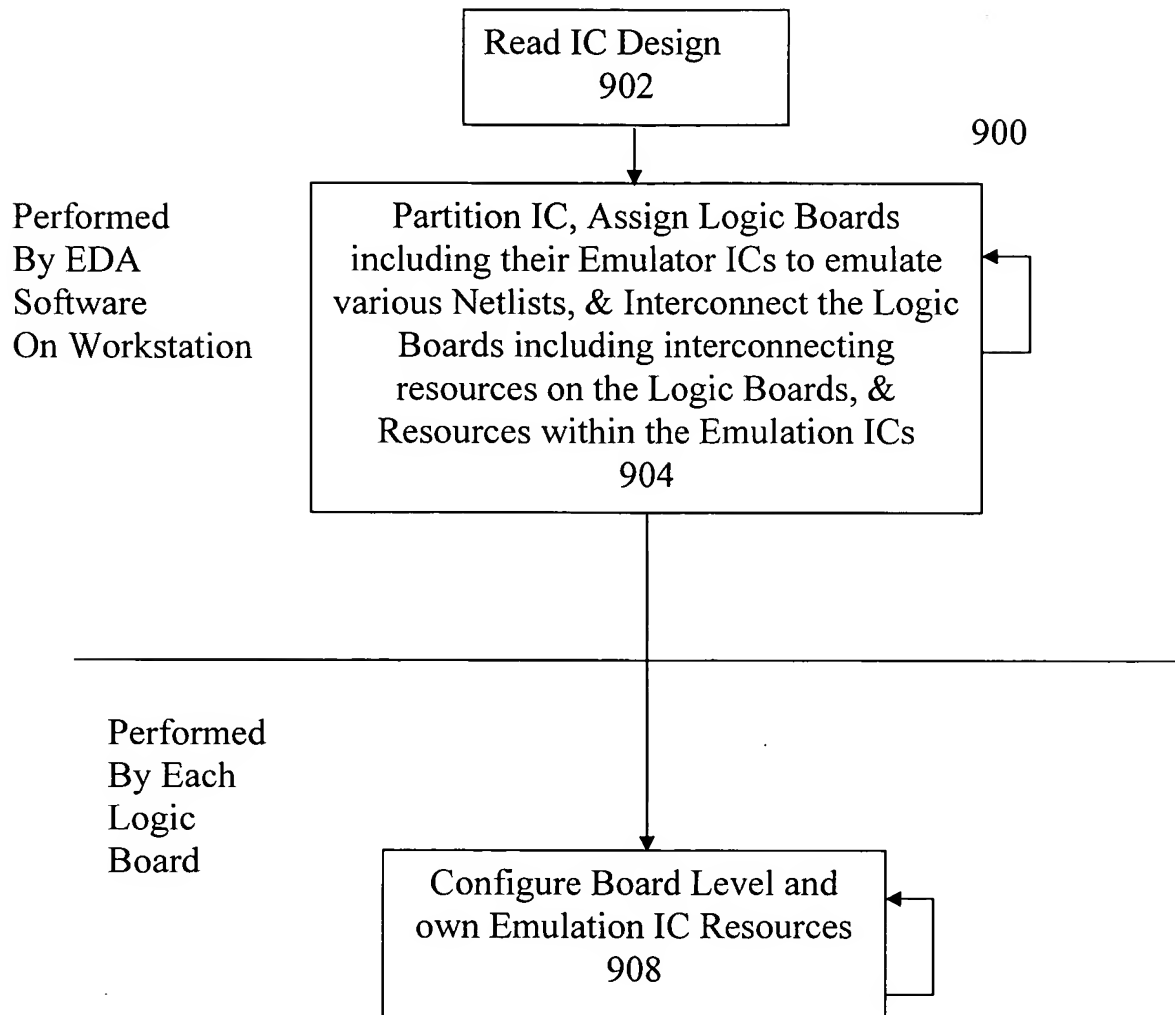
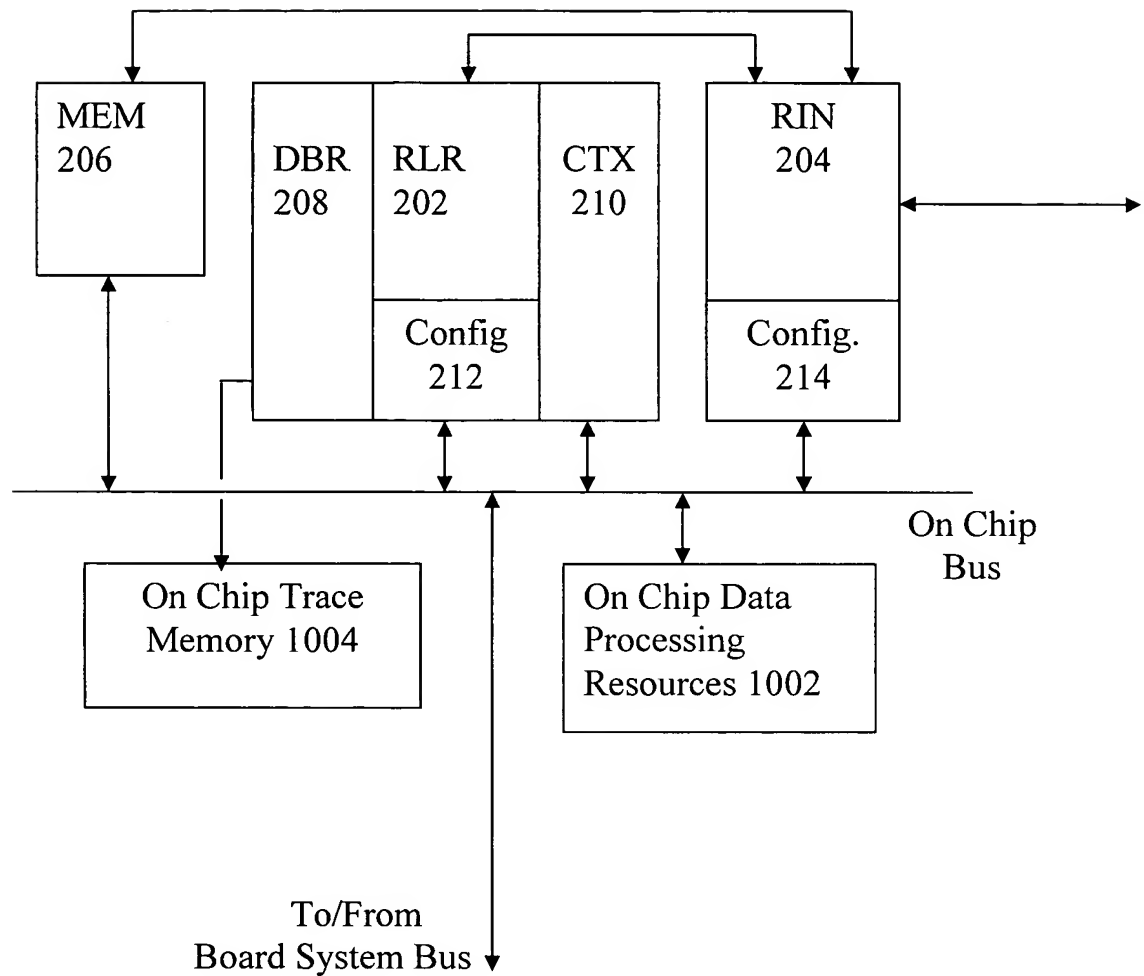


Figure 9



RLR = Reconfigurable Logic Resources
 RIN = Reconfigurable Interconnect Network
 MEM = Memory
 DBR = Debugging Resources
 CTX = Context (State) Elements
 Config = Configuration registers.

Figure 10